

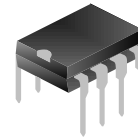
CURRENT MODE PWM CONTROLLER

DESCRIPTION

SDH8303 is a current mode PWM controller with built-in high-voltage MOSFET used for SMPS.

SDH8303 has a built-in high-voltage startup circuit. It enters burst mode at light load to reduce the system standby power dissipation; the frequency reduction function optimizes the conversion efficiency at light load; frequency jitter improves EMI characteristics; the soft startup function reduces the device stress to avoid transformer saturation; the VDD burst function not only prevents VDD restart at undervoltage condition, but also reduces standby power dissipation.

SDH8303 integrates various protections, including VDD undervoltage protection, VDD overvoltage protection, lead edge blanking, output short-circuit protection, over current protection, over temperature protection, etc. Meanwhile, the circuit will continue to restart automatically until the system restore.



DIP-8-300-2.54

FEATURES

- ◆ High-voltage startup
- ◆ Burst mode at light load
- ◆ Frequency reduction
- ◆ Frequency jitter
- ◆ Soft startup
- ◆ VDD burst
- ◆ VDD undervoltage protection
- ◆ VDD overvoltage protection
- ◆ Lead edge blanking
- ◆ Output short-circuit protection
- ◆ Over current protection
- ◆ Over temperature protection

APPLICATIONS

- ◆ Off-line SMPS
- ◆ Non-isolated buck-boost converter
- ◆ Small house-hold appliances

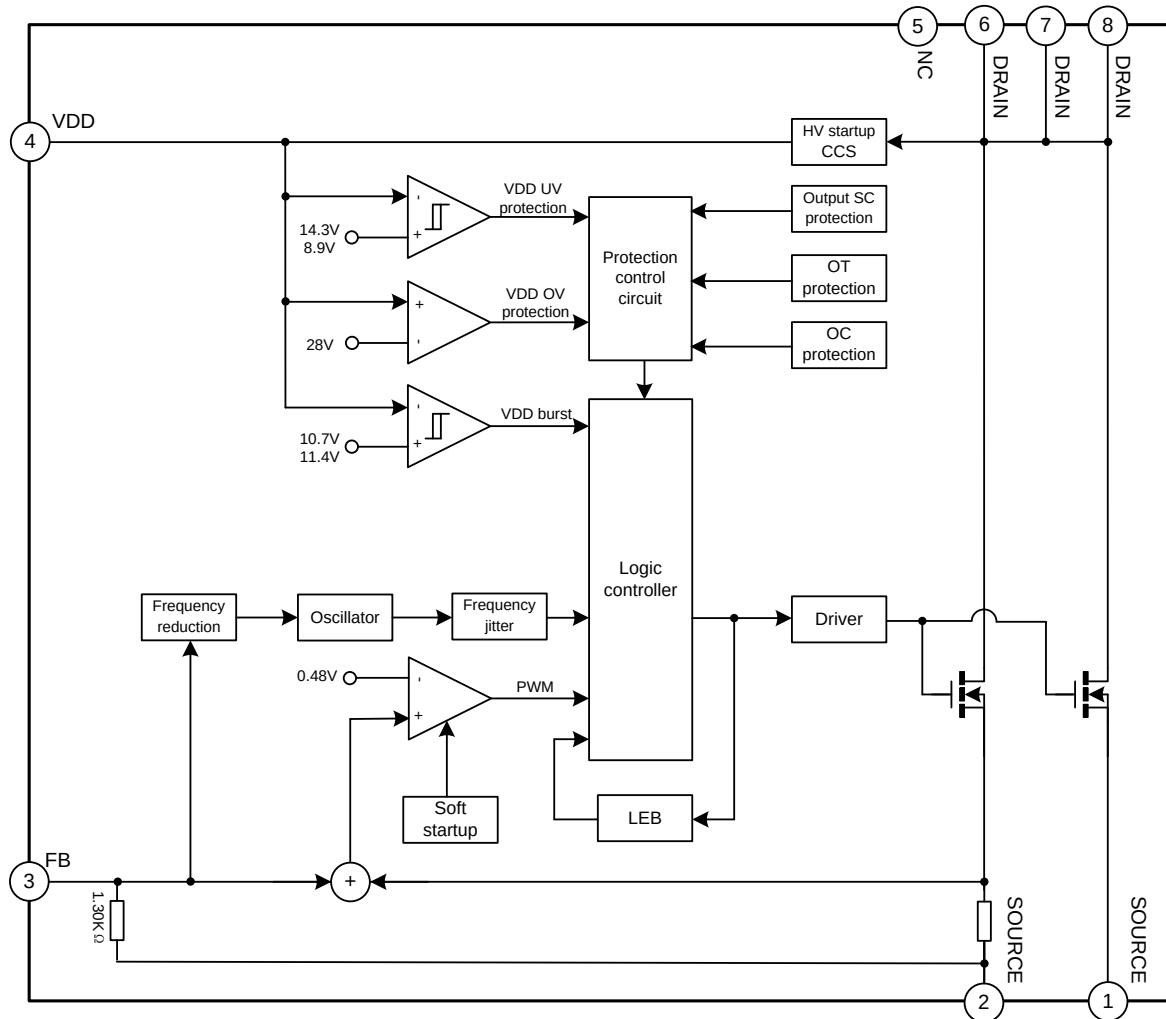
ORDERING INFORMATION

Part No.	Package	Marking	Hazardous Substance Control	Packing
SDH8303	DIP-8-300-2.54	SDH8303	Halogen free	Tube

TYPICAL OUTPUT POWER CAPABILITY

Part No.	195~265V		85~265V	
	Adaptor	Open type	Adaptor	Open type
SDH8303	12W	20W	7W	12W

BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATING

Characteristics	Symbol	Ratings	Unit
Drain-gate voltage (RGS=1MΩ)	V_{DGR}	650	V
Gate-source (ground) voltage	V_{GS}	±30	V
Drain current pulse ^{††*}	I_{DM}	6	A
Drain continuous current (Tamb=25°C)	I_D	1	A
Signal pulsed avalanche energy ^{**}	EAS	30	mJ
High-voltage input	$V_{HV,MAX}$	650	V

Characteristics	Symbol	Ratings	Unit
Power voltage	$V_{DD,MAX}$	30	V
Feedback current	I_{FB}	3	mA
Power dissipation	P_D	6.3	W
Thermal resistance junction-ambient	θ_{ja}	70	°C/W
Thermal resistance junction-case	θ_{jc}	20	°C/W
Operating junction temperature	T_J	+150	°C
Operating temperature range	T_{amb}	-20~+85	°C
Storage temperature range	T_{STG}	-55~+150	°C

*The pulse width is determined by maximum junction temperature.

** L=51mH, $T_J=25\text{ }^{\circ}\text{C}$ (start)

MOSFET ELECTRICAL CHARACTERISTICS (unless otherwise specified, $T_{amb}=25^{\circ}\text{C}$)

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Drain-source break-down voltage	BV_{DSS}	$V_{GS}=0V, I_D=50\mu A$	650	--	--	V
Zero gate voltage drain current	I_{DSS}	$V_{DS}=650V, V_{GS}=0V$	--	--	50	μA
		$V_{DS}=480V, V_{GS}=0V, T_{amb}=125^{\circ}\text{C}$	--	--	200	μA
Static drain-source on resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=0.5A$	7	10	13	Ω
Input capacitance	C_{ISS}	$V_{GS}=0V, V_{DS}=25V, f=1\text{MHz}$	--	155	--	pF
Output capacitance	C_{OSS}	$V_{GS}=0V, V_{DS}=25V, f=1\text{MHz}$	--	23	--	pF
Reverse transfer capacitance	C_{RSS}	$V_{GS}=0V, V_{DS}=25V, f=1\text{MHz}$	--	0.6	--	pF
Turn-on delay time	$T_{D(ON)}$	$V_{DD}=0.5BV_{DSS}, I_D=25\text{mA}$	--	6	--	ns
Rise time	T_R	$V_{DD}=0.5BV_{DSS}, I_D=25\text{mA}$	--	13	--	ns
Turn-off delay time	$T_{D(OFF)}$	$V_{DD}=0.5BV_{DSS}, I_D=25\text{mA}$	--	9	--	ns
Fall time	T_F	$V_{DD}=0.5BV_{DSS}, I_D=25\text{mA}$	--	17	--	ns

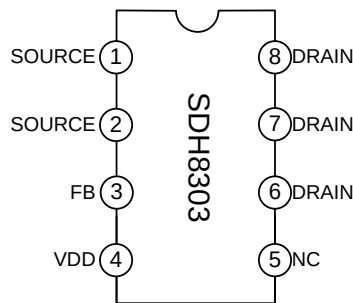
ELECTRICAL CHARACTERISTICS (unless otherwise specified, $V_{DD}=18V, T_{amb}=25^{\circ}\text{C}$)

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
HV startup						
Charging current	I_{HVC}	$V_{DD}=0V, V_{HV}=100V$	--	1	--	mA
Shutdown drain current	I_{HVS}	$V_{DD}=18V, V_{HV}=700V$	--	3	--	μA
Operating voltage						
Operating voltage range	VDD	After startup	10	--	25	V
Operating current 0	IDD0	$I_{FB} = 1.2\text{mA}$	--	0.8	--	mA
Operating current 1	IDD1	$I_{FB} = 0.55\text{mA}$	--	1.2	--	mA
VDD on voltage	V_{DDON}	$I_{FB} = 0.5\text{mA}$	13.6	14.3	15	V

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
VDD under voltage protect voltage	VDD _{OFF}	I _{FB} = 0.5mA	8.3	8.9	9.5	V
VDD threshold hysteresis	VDD _{HYS}		4.1	5.4	6.7	V
VDD over voltage protect voltage	VDD _{OV}	I _{FB} = 0.5mA	--	28	--	V
VDD clamp voltage	VDD _{CLAMP}		--	30	--	V
VDD burst threshold	VDD _{LOW}	I _{FB} = 1.5mA	--	10.7	--	V
VDD burst threshold hysteresis	VDD _{LOWHYS}	I _{FB} = 1.5mA	--	0.7	--	V
Operating frequency						
Normal switching frequency	f _{SW1}	I _{FB} = 0.5mA	54	60	66	KHz
Frequency jitter range	f _{JIT}	I _{FB} = 0.5mA	-2	--	2	KHz
Modulation frequency	f _{MOD}	I _{FB} = 0.5mA	--	250	--	Hz
Max. duty ratio	D _{MAX}	I _{FB} = 0.5mA	75	80	85	%
FB frequency reduction start threshold	IFB _{FD1}		--	625	--	uA
FB frequency reduction end threshold	IFB _{FD2}		--	695	--	uA
Switching frequency after frequency reduction	f _{SW2}	I _{FB} = 0.8mA	--	20	--	KHz
Feedback input						
FB input impedance	R _{FB}		--	1.30	--	kΩ
FB turn-off current	IFB _{SD}		0.74	0.85	1	mA
FB turn-off current hysteresis	IFB _{SDHYS}		--	70	--	uA
FB over load protect detecting current	IFB _{OLP}		--	450	--	uA
FB over load protect operating current	ID _{FBOLP}	I _{FB} = 0.35mA	--	3.5	--	mA
Peak current limit						
Peak limiting threshold	I _{PKLIM}	I _{FB} = 0mA	600	700	800	mA
Current gain	G _{ID}		--	700	--	A/A
Current sensing turn-off delay	t _d		--	250	--	ns
LEB time	t _{LEB}		--	300	--	ns
Min. turn-on time	t _{ONMIN}		--	550	--	ns
Soft startup time	t _{SS}	I _{FB} = 0.55mA	--	9	--	ms

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Over temperature protection						
Over temperature detection	T_{SD}		--	145	--	°C
Over temperature hysteresis	T_{HYS}		--	25	--	°C

PIN CONFIGURATION



PIN DESCRIPTION

Pin No.	Pin Name	I/O	Function description
1, 2	SOURCE	I/O	Source of power MOSFET and reference ground of control circuit
3	FB	I	Feedback input
4	VDD	I/O	Power supply for control circuit
5	NC	-	Floating
6, 7, 8	DRAIN	I/O	Drain of power MOSFET

FUNCTION DESCRIPTION

SDH8303 is a current mode PWM controller with built-in high-voltage MOSFET used for SMPS.. It features a built-in high-voltage startup circuit, burst mode at light load, functions of frequency reduction, frequency jitter, soft startup, VDD burst. Additionally, it offers complete protection coverage with VDD undervoltage protection, VDD overvoltage protection, leading edge blanking, output short-circuit protection, over current protection, over temperature protection, etc.

High-voltage startup

SDH8303 includes a high-voltage startup circuit. When the IC is powered, AC input voltage activates the built-in constant current source from DRAIN pin, and charges the VDD capacitor C1 with charging current of 1mA. When the VDD voltage reaches to on voltage 14.3V, the high-voltage start-up constant current source is cut off, VDD is no longer charged by DRAIN, but is powered by auxiliary winding L1 through diode D1; if VDD voltage falls to UVP voltage 8.9V, the high-voltage start-up constant current source is activated again, and charges VDD via DRAIN till VDD voltage reaches to on voltage 14.3V.

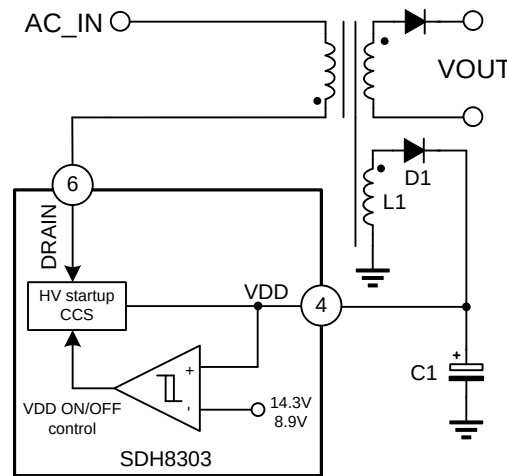


Figure 1 High-voltage startup circuit

Burst mode at light load

At light load, if the output VOUT is too high, the input current IFB fed to FB pin exceeds the shut-down current 0.85mA, then SDH8303 turns off the switch to decrease VOUT; when VOUT drops till IFB decreases by 70uA, SDH8303 opens the switch again to increase VOUT; repeat the foregoing process to enter burst mode, so decreases switch times for low system standby power dissipation.

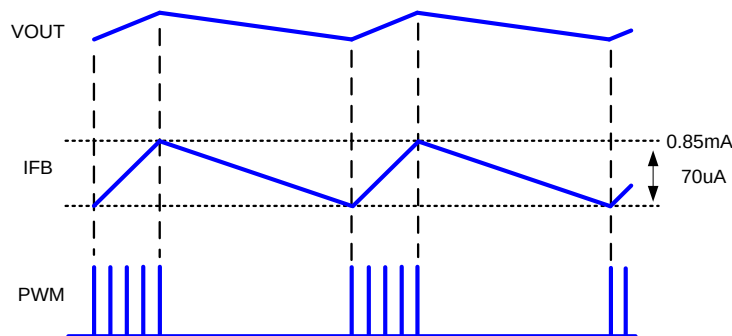


Figure 2 Waveforms at burst mode

Frequency reduction

In order to optimize the conversion efficiency at light load, SDH8303 adopts frequency reduction mode, i.e., adjusting the switching frequency through detecting the current IFB input to FB pin. When IFB is smaller than frequency reduction start threshold 625uA, the switching frequency is 60KHz; when IFB increases from 625uA to 695uA (frequency reduction end threshold), the switching frequency decreases linearly from 60KHz to 20KHz; if IFB is larger than 695uA, the switching frequency is 20KHz.

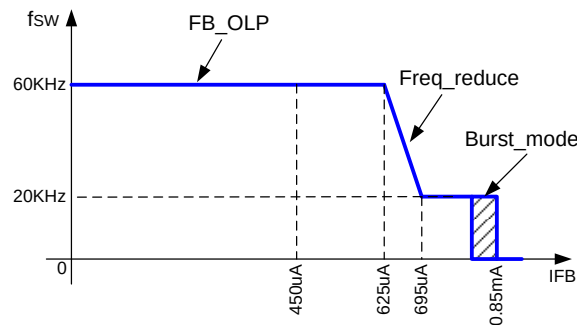


Figure 3 Frequency reduction curve

Frequency jitter

SDH8303 adopts frequency jitter technology to reduce EMI, namely, making the switching frequency constantly changing in jitter frequency range of $\pm 2\text{KHz}$ at 250Hz modulation frequency to reduce the radiation at a certain single frequency.

Soft startup

SDH8303 enables the maximum peak current of DRAIN to increase restrictedly step by step within soft startup time, hence, it can reduce the stress of devices and prevent the transformer from turning into saturation.

VDD burst

SDH8303 at light load or the state switching between full load and no load, when the switch is turned off, enabling VDD voltage to drop to burst threshold (10.7V), the switch is turned on, making VDD voltage rising; when VDD voltage increases by 0.7V, the switch is off again, VDD drops; repeat the foregoing process to enter burst mode, VDD voltage will not decrease to UVP voltage (8.9V), which not only prevents VDD restart at undervoltage condition, but also reduces the standby power dissipation.

VDD undervoltage protection

The MOSFET is off if SDH8303 is at abnormal state, which causes VDD voltage to fall. When VDD reaches to the UVP voltage (8.9V), the built-in high-voltage start-up constant current source is activated, then VDD is charged by the input AC voltage, once it increases to UVP stop voltage as on voltage (14.3V), the SDH8303 resumes normal operation, then the circuit is able to auto restart after fault is cleared.

VDD overvoltage protection

SDH8303 turns off the switch once the VDD voltage reaches to OVP voltage (28V), and is locked out till the VDD voltage decreases to UVP voltage (8.9V), and then the circuit restarts.

Leading edge blanking

At the instant of internal MOSFET on, a high current spike will appear since the existed parasitic capacitor at DRAIN. If this signal is sensed, the overcurrent protection will be activated. To avoid this false trigger, SDH8303 senses the current of DRAIN pin a LEB time of 300ns after the MOSFET is turned on.

Output short-circuit protection

Once the current input to FB pin is less than 450uA, the short-circuit protection is activated, decreasing VDD voltage to UVP voltage (8.9V), and then the circuit restarts.

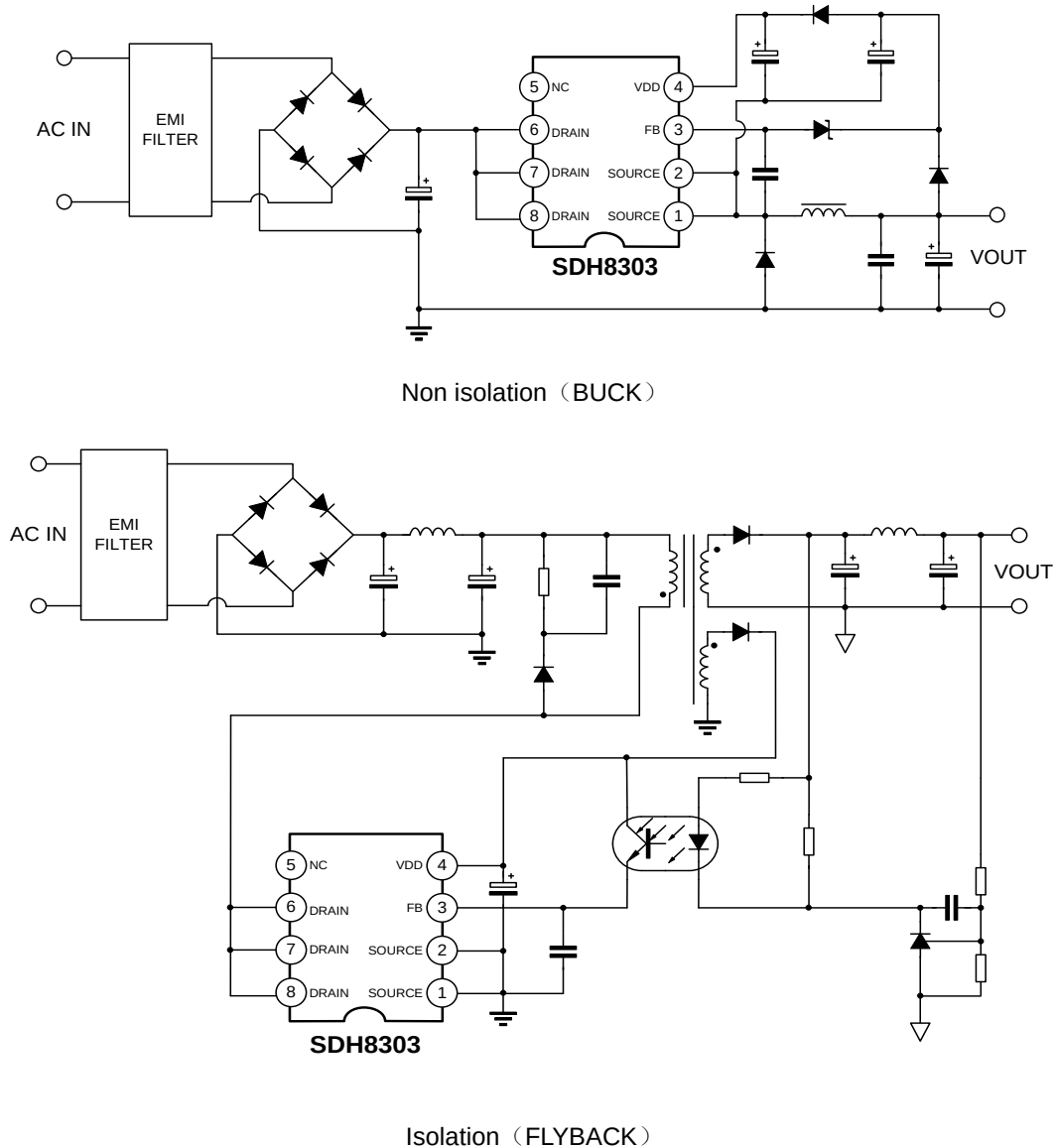
Over current protection

If an over current fault is sensed at DRAIN pin, the switching times are reduced and turned to normal until the fault is cleared.

Over temperature protection

SDH8303 turns off the switch once the temperature reaches to OTP threshold 145°C, and turns on the switch if the temperature decreases by 25°C.

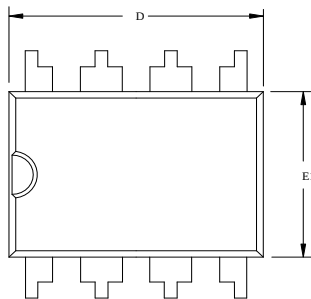
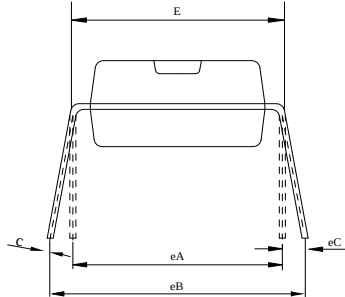
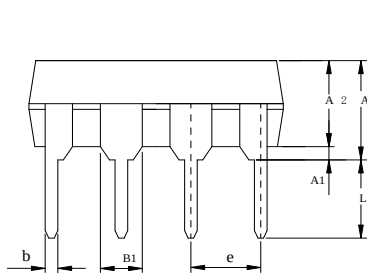
TYPICAL APPLICATION CIRCUIT



PACKAGE OUTLINE

DIP-8-300-2.54

UNIT: mm



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	—	—	4.10
A1	0.50	—	—
A2	2.90	3.30	3.40
b	0.38	0.46	0.55
B1	1.22	1.52	1.82
c	0.20	0.25	0.32
D	9.00	9.40	9.80
E	7.62	7.87	8.26
E1	6.10	6.35	6.60
e	2.54BSC		
eA	7.62BSC		
eB	7.62	—	9.30
eC	0	—	1.52
L	3.00	—	—



MOS DEVICES OPERATE NOTES:

Electrostatic charges may exist in many things. Please take following preventive measures to prevent effectively the MOS electric circuit as a result of the damage which is caused by discharge:

- The operator must put on wrist strap which should be earthed to against electrostatic.
- Equipment cases should be earthed.
- All tools used during assembly, including soldering tools and solder baths, must be earthed.
- MOS devices should be packed in antistatic/conductive containers for transportation.

Disclaimer :

- Silan reserves the right to make changes to the information herein for the improvement of the design and performance without prior notice! Customers should obtain the latest relevant information before placing orders and should verify that such information is complete and current.
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Part No.:	SDH8303	Document Type:	Datasheet
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Rev.: 1.1

Revision History:

1. delete VDD latchup function
2. Modify the description
3. Modify the features
4. Modify the block diagram
5. Modify the electrical characteristic
6. Modify the function description
7. Update package outline: Delete values of d and Ø; add values of E、eA、eB、eC; modify values of A2、b、B1、c、D、E

Rev.: 1.0

Revision History:

1. First release
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